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Figure 1: Development Flow

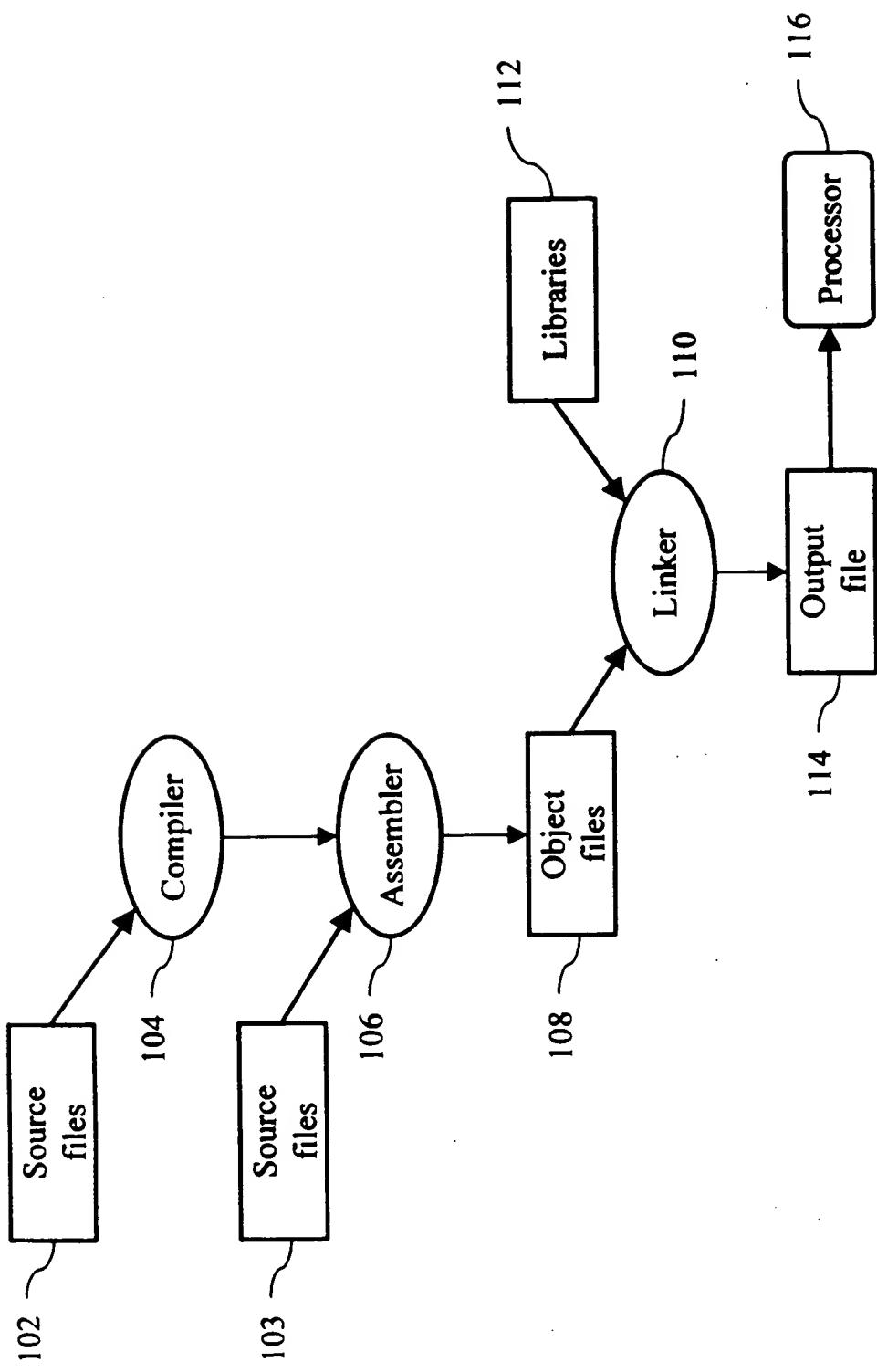


Figure 2A: Typical Linker Apparatus

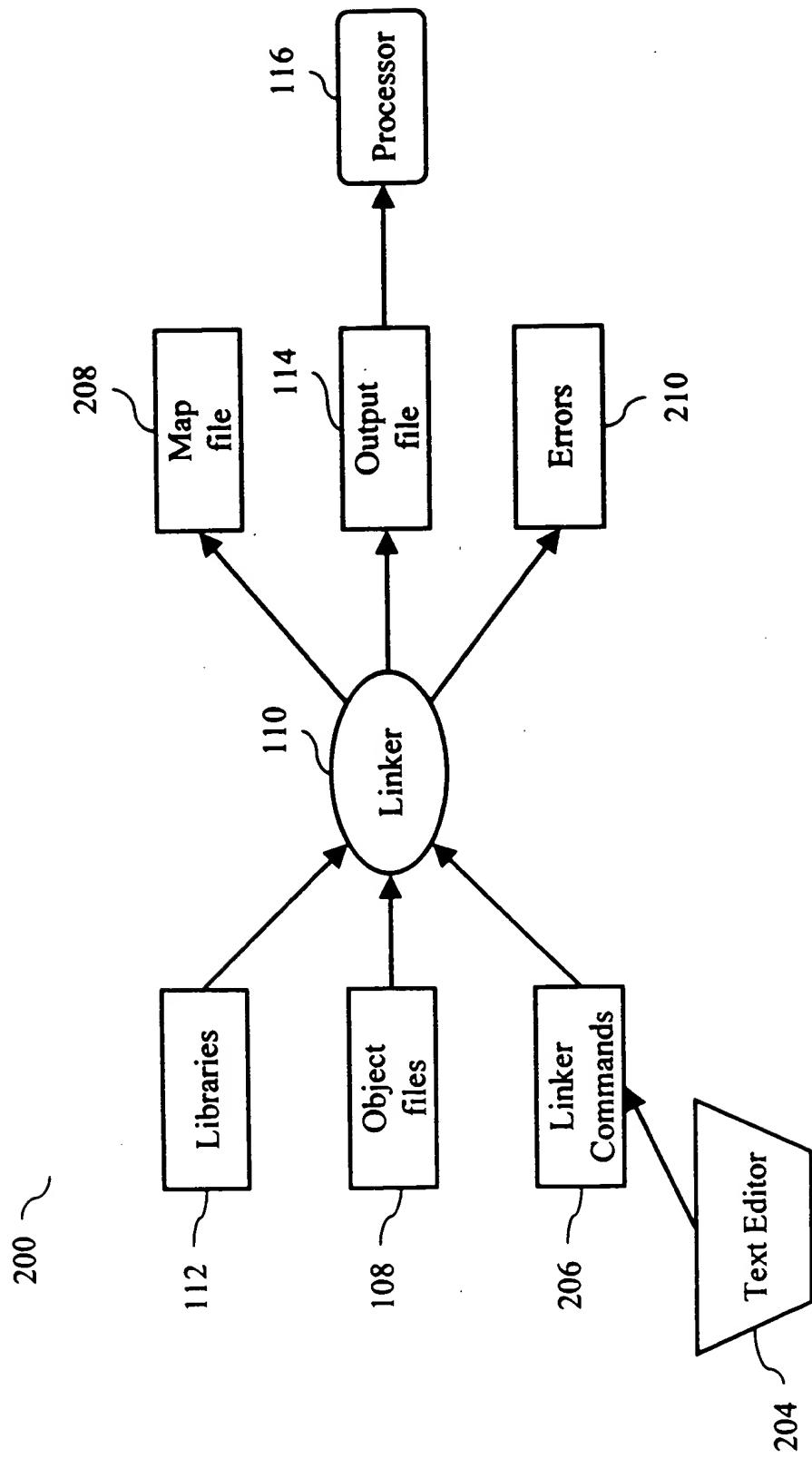
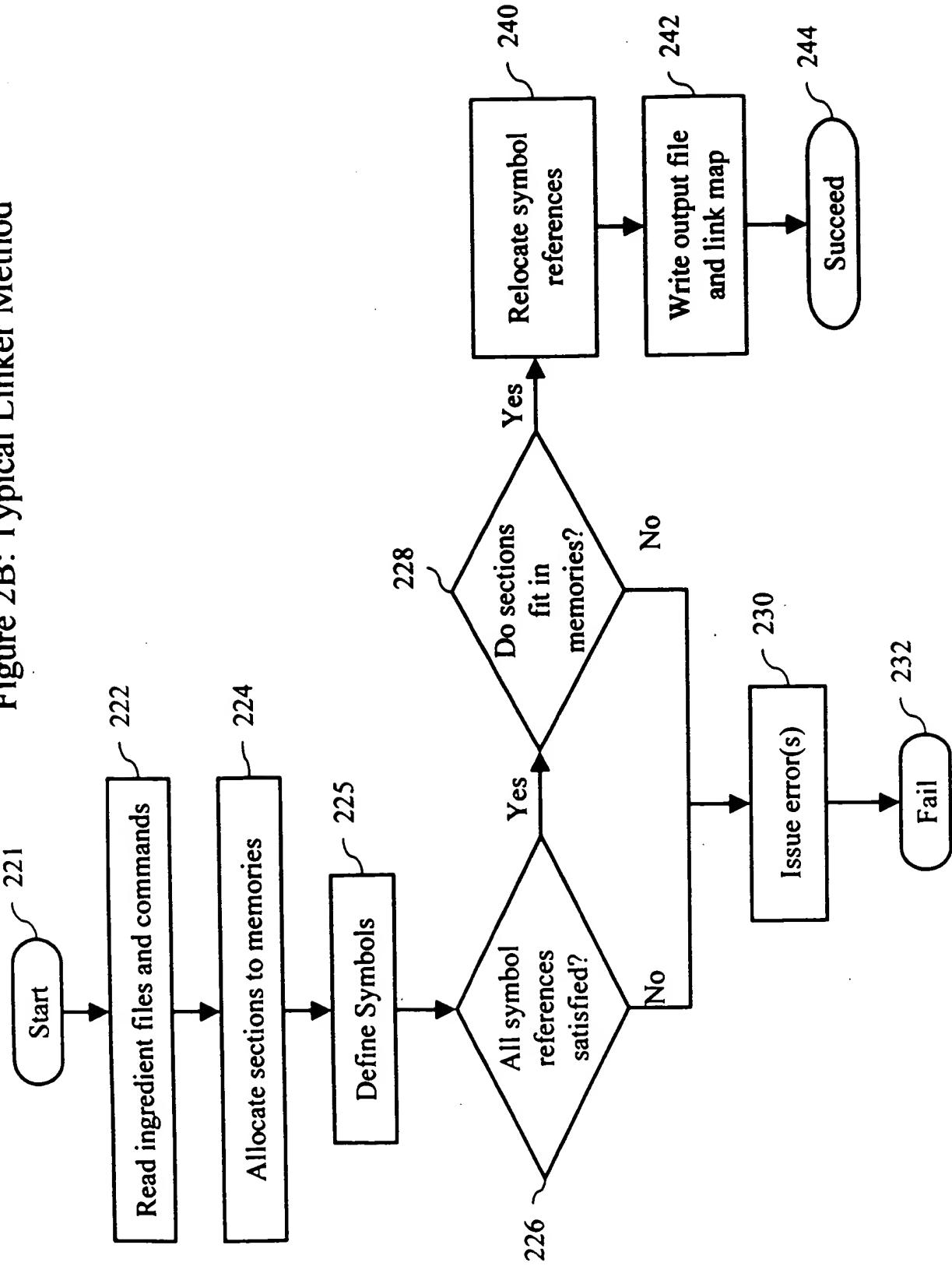
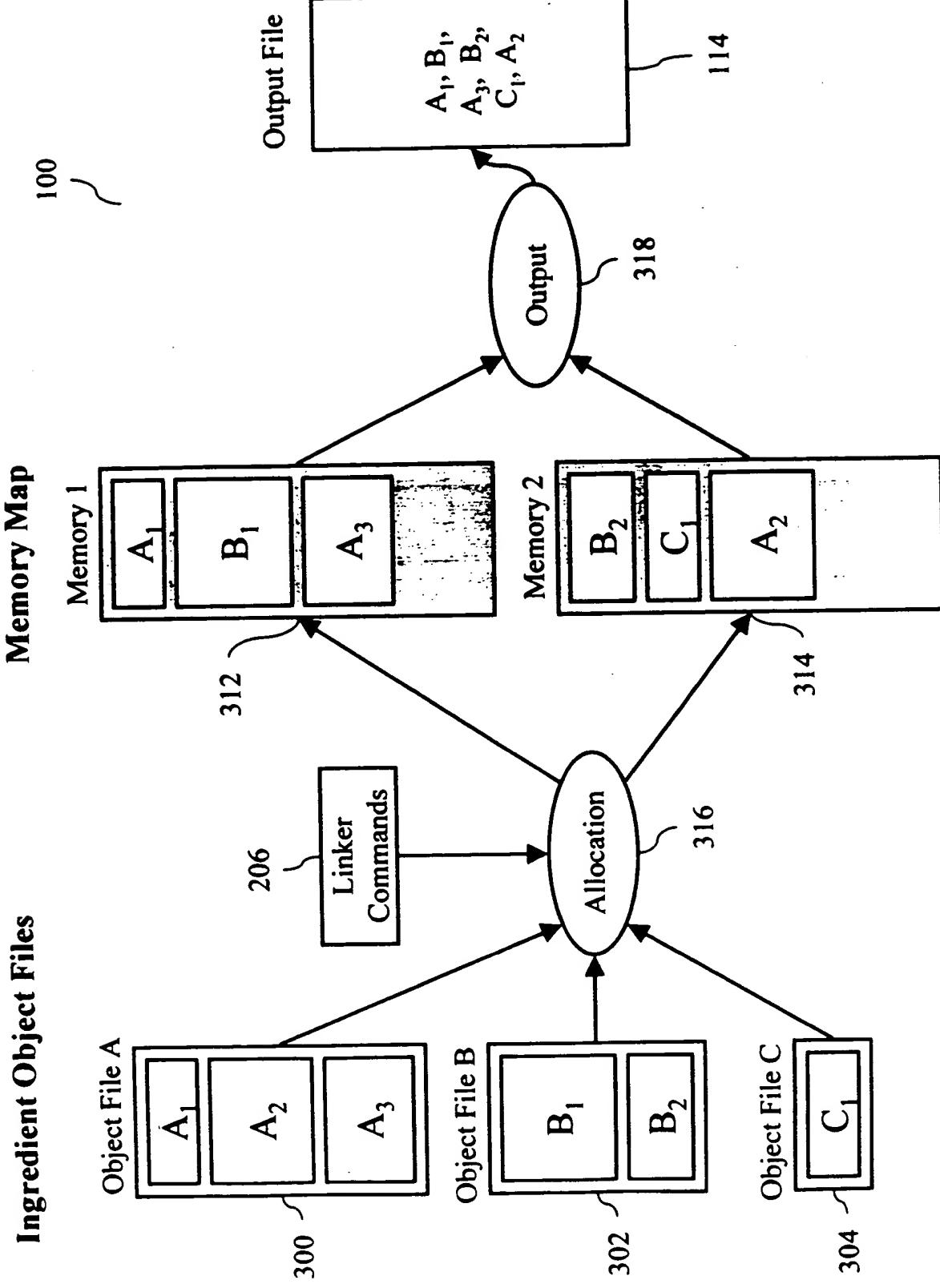


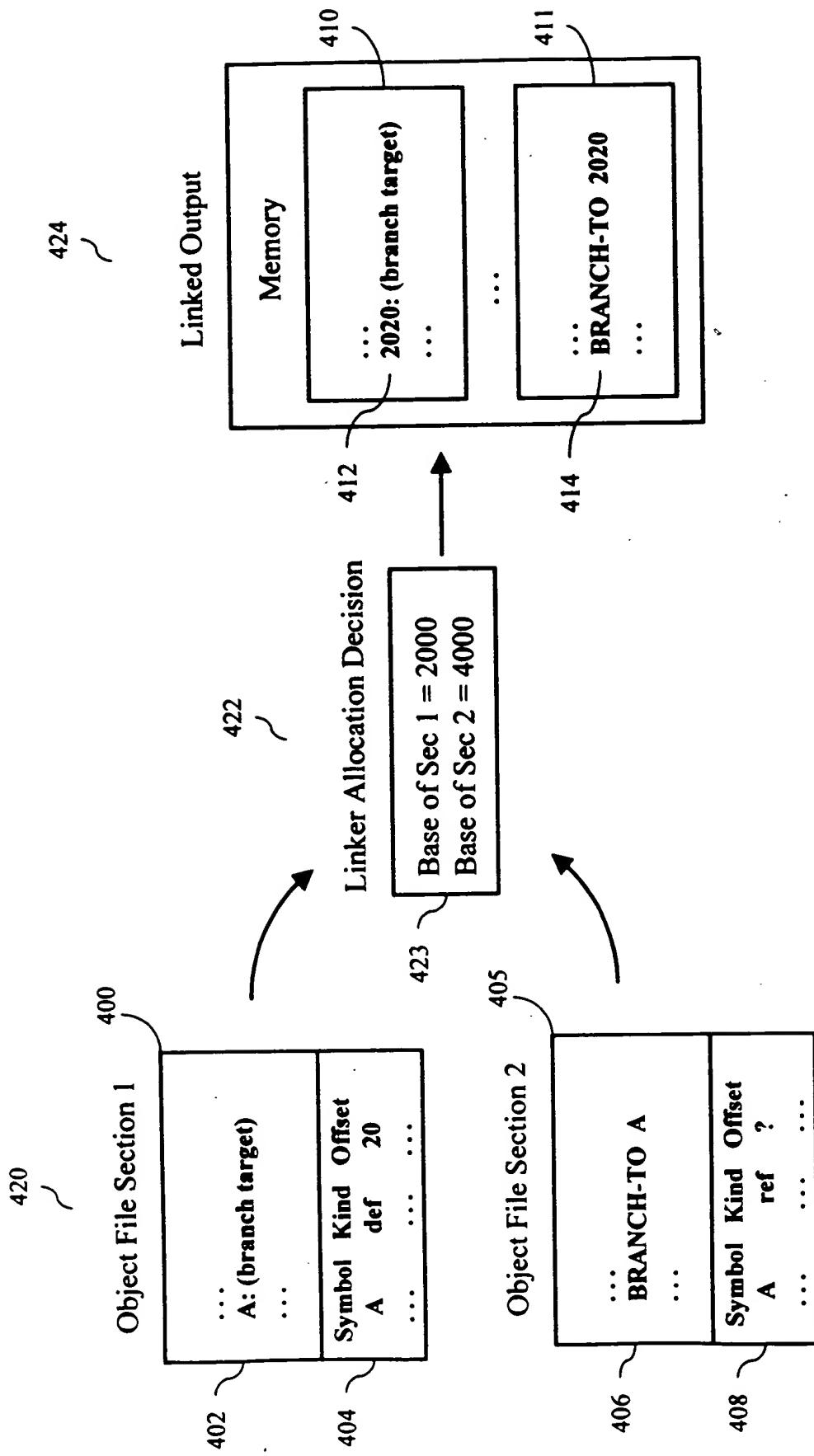
Figure 2B: Typical Linker Method



**Figure 3: Linker Allocation and Output**



**Figure 4: Linker Symbol Resolution**



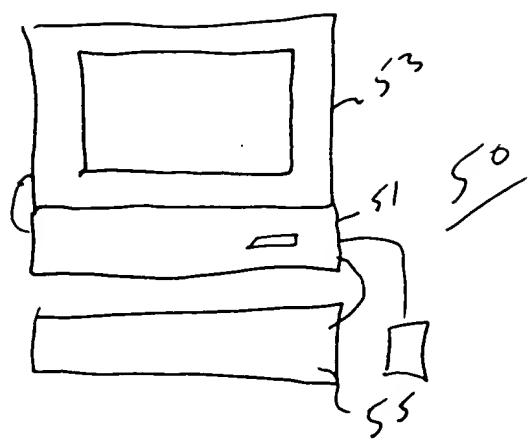


Fig. 5

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009290"ETFOGIC

Figure 6: Original code as produced by compiler or assembler.

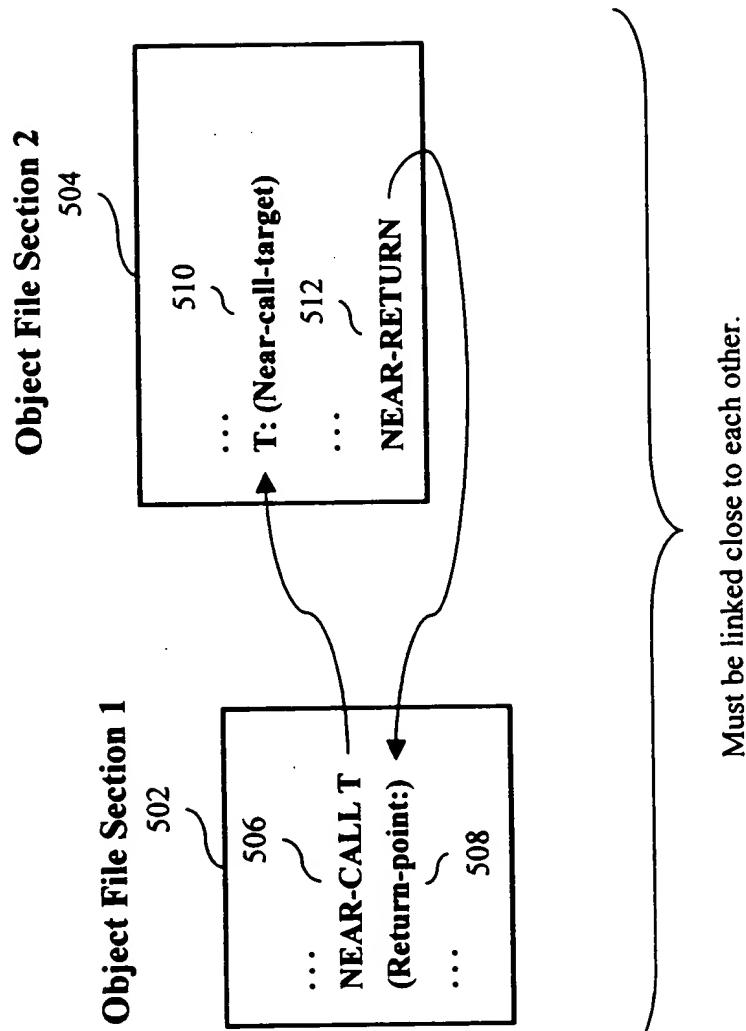


Figure 7: Near call after fixup with single trampoline.

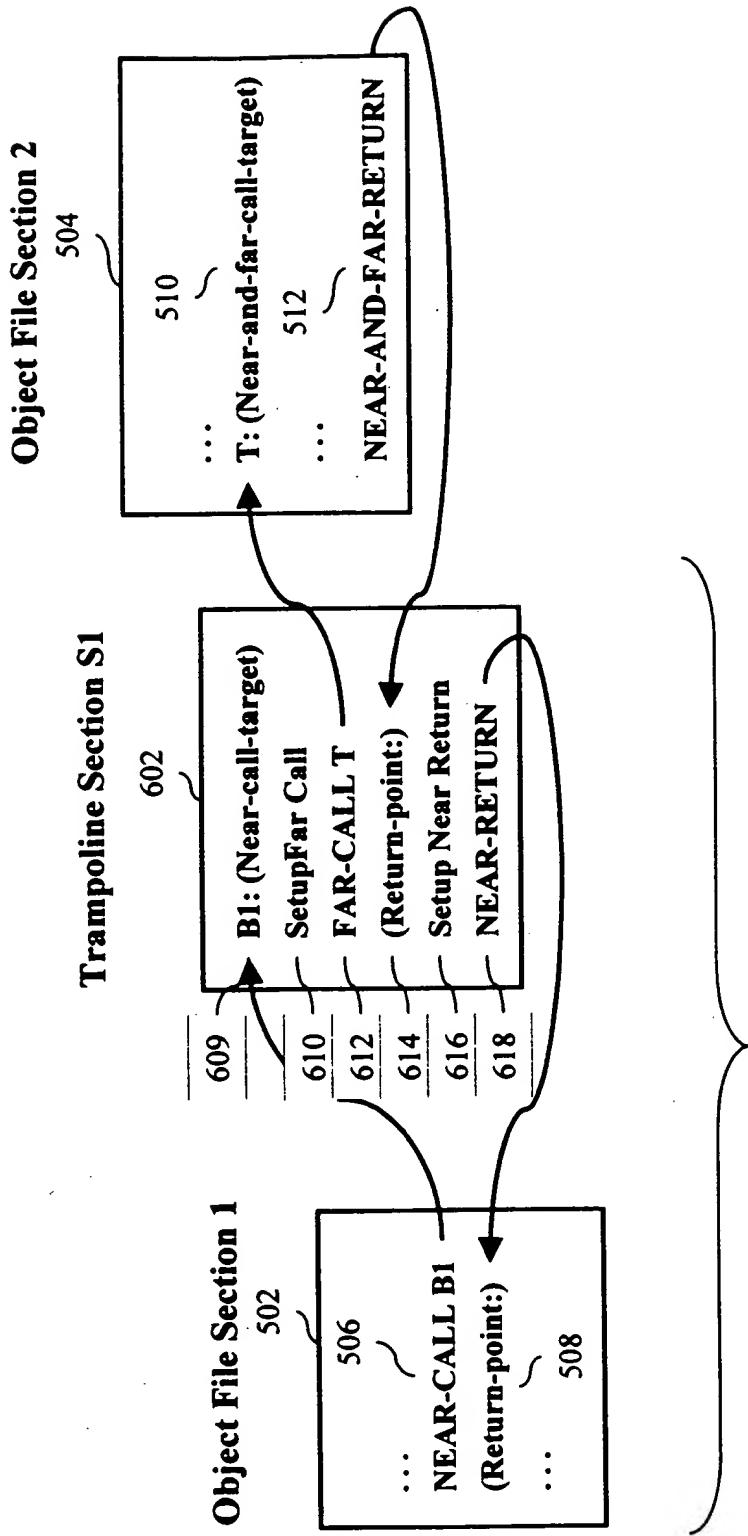
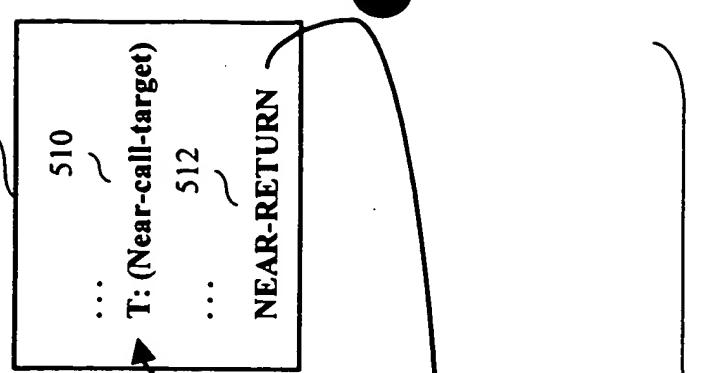


Figure 8: Call after fixup with two trampolines.

DISASSEMBLY EDITION

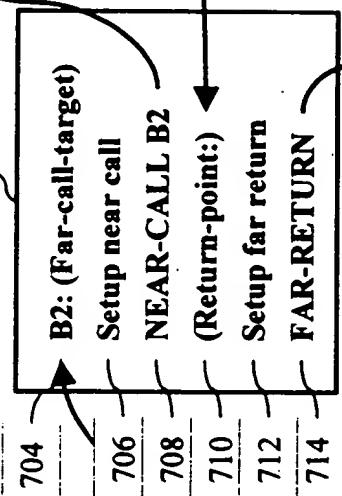
Object File Section 2

504



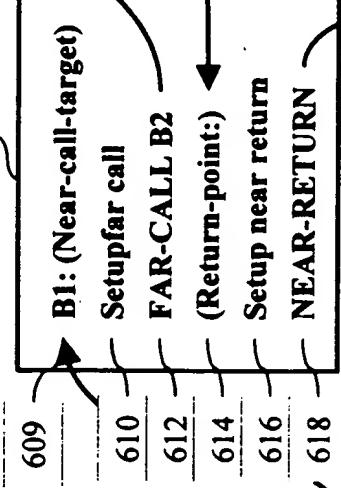
Trampoline Section S1

702



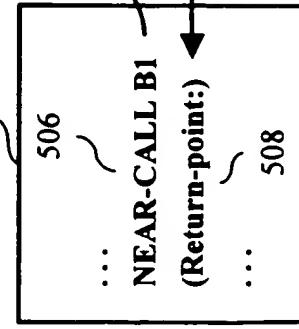
Trampoline Section S1

602



Object File Section 1

502



Must be linked close to each other.

Must be linked close to each other.

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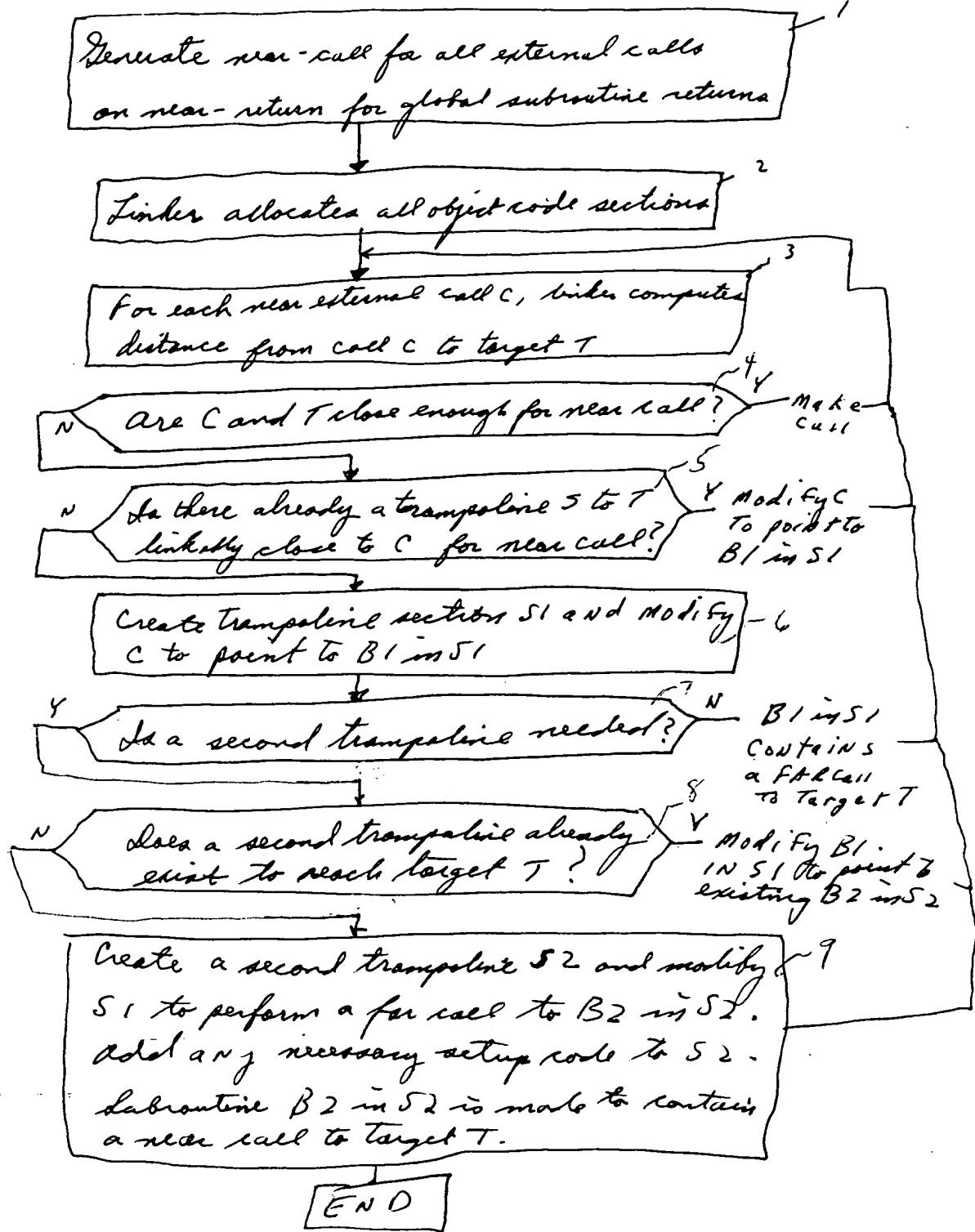
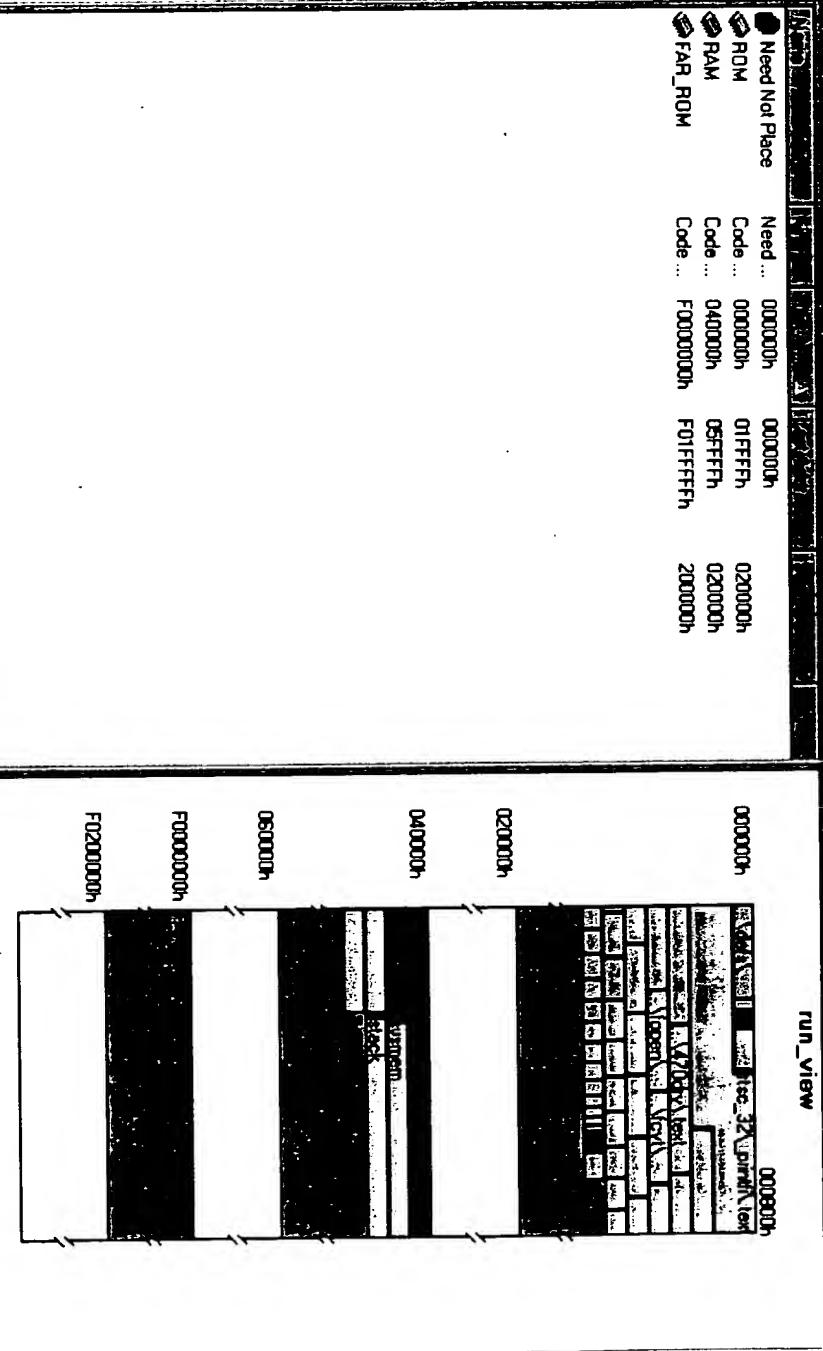


Fig. 9

## TRAMPOLINES

Fig. 10 (10)



```
INFO completed upload ingredients from legacy linker data structures in under a second
INFO legacy linker successfully loaded ingredients
i INFO begin parsing \TMS470R1x.mem
i INFO done parsing \TMS470R1x.mem
i INFO successfully completed parse of command line & files
i INFO
i INFO parsing strategy D:\sb\Nameless\Ncl_otp\Ncl.str
i INFO done parsing strategy D:\sb\Nameless\Ncl_otp\Ncl.str
i INFO begin executing strategy D:\sb\Nameless\Ncl_otp\Ncl.str
i INFO completed executing strategy D:\sb\Nameless\Ncl_otp\Ncl.str in under a second
```

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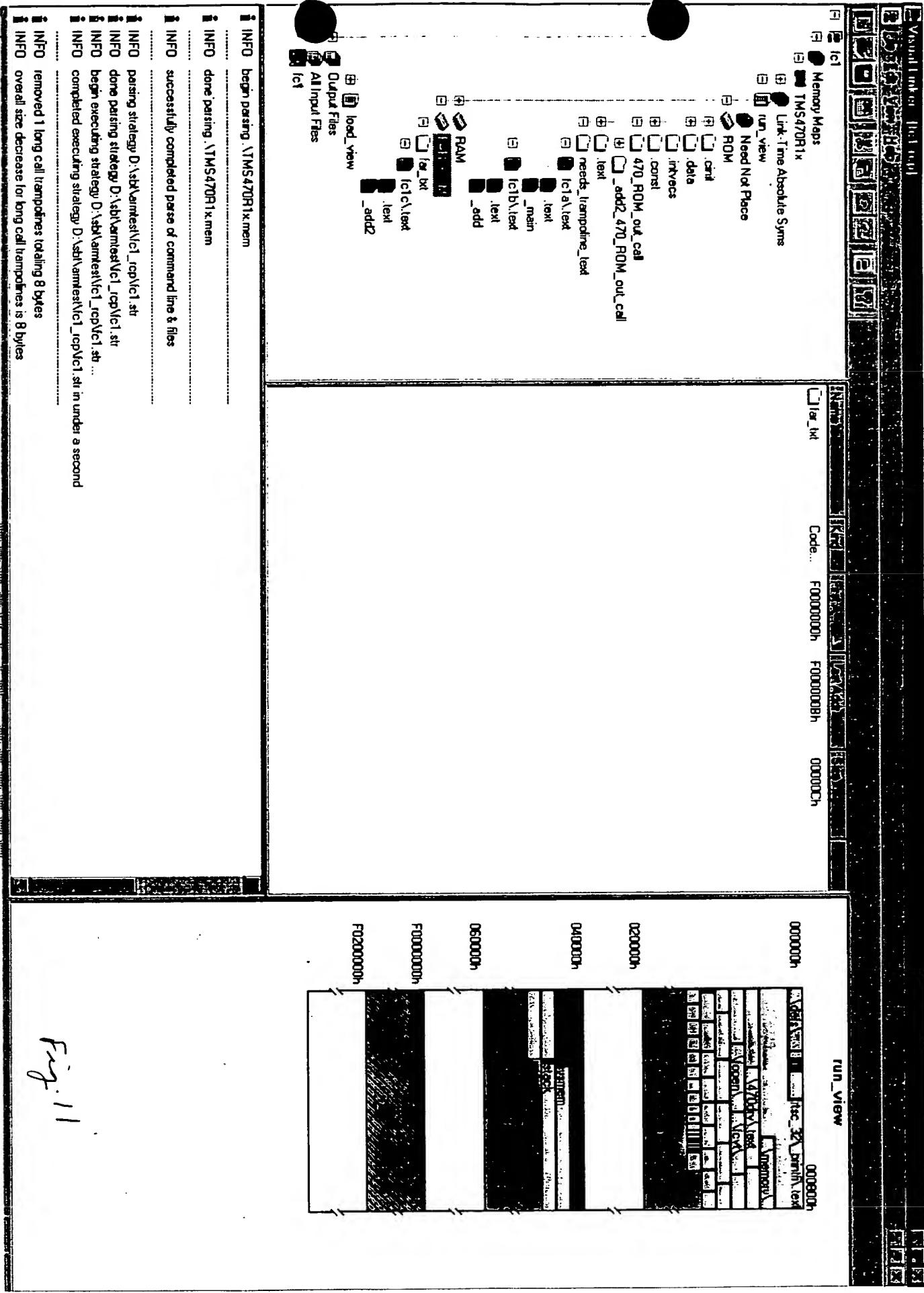


Fig. 11

